



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of

MIURA

Application Number: 10/809,465

Filed: March 26, 2004

**For: STORAGE ARRAY CONTROLLER WITH A
NONVOLATILE MEMORY AS A CACHE MEMORY
AND CONTROL METHOD OF THE SAME (AS
AMENDED)**

Attorney Docket No. ASAM.0117

**Honorable Assistant Commissioner
for Patents
Washington, D.C. 20231**

Art Unit 2186

**Examiner
Elmore, Stephen C.**

RESPONSE AND AMENDMENT UNDER 37 C.F.R. § 1.111

Sir:

This is in response to the Office Action mailed on July 28, 2005 and the telephone interview with the Examiner on September 23, 2005, the period of response to which is set to expire on October 28, 2005. Please amend the above identified application as follows: